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DOPES (DIAGNOSTICS OF POWER ELECTRONIC SYSTEMS) PROJECT

Progress Report 4

Shamar Christian

MILESTONE SUMMARY

Month	Deliverable	Responsibility	Update
March	Version 3 of PCB (Physical)	Shamar Christian	Awaiting delivery of PCB's
April	Voltage Regulator	Shamar Christian	Design complete, simulation to be verified and implemented

ORIGINAL DESIGN



PREVIOUS DESIGN SOLUTION



 Modified transistor
placement
Capacity for sensor self
mount

SCHEMATIC FOR DESIGN



- Acquisition of EPC2001 model
- Free form layout for customizati on

ACTUAL IMPLEMENTATION (REAL WORLD VIEW)



SIMULATION AND VERIFICATION



VOLTAGE REGULATION

• Protection of components

• Precision



DESIGN CONSIDERATIONS

- Bias Supply voltage range (4.5-5.5V)
- Hysteresis functions



ACTIVITY SUMMARY

<u>Highlights</u>

- Completed simplified circuit redesign to produce same results
- Budgetary responsibility
- Design simulated to ensure integrity
- Design completed for v4 possibly

Lowlights

- Simulation Precision
- Time taken for PCB to arrive

RISK MANAGEMENT

Rank Risk Approach
Develop
Voltage Regulator Alternatives/Look
1 Malfunction for existing ones
Altered Circuit Simulate and
2 functionality modify design
Implement
Time for previous design
implementing but take
3 newer design precaution
Low accuracy data Modify Sensor z
4 yield position



Impact

PLANNED ACTIVITY FOR NEXT PERIOD

- Full implementation of redesigned circuit in current experimental setup
- Subject voltage regulator to more simulation for submission
- Implement voltage regulator design in v4 possibly

QUESTIONS AND COMMENTS